

1756

520.41109X00



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shunichi MATSUMOTO, et al.
Application No.: 10/052,513
Filed: January 23, 2002
For: METHOD AND SYSTEM FOR PROCESSING A
SEMI-CONDUCTOR DEVICE
Art Unit: 1756
Examiner: Christopher G. Young

SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 12, 2004

Sir:

In supplement to the Amendment filed on July 6, 2004, please further amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims;

Amendments to the Drawings describing changes to the drawings shown in the Appendix;

Remarks are included following the amendments;

An Appendix including replacement drawing figures is attached following the Remarks.